

Modeling and Optimization Techniques of Electronic Devices Using Genetic Algorithm

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A new method for developing a realistic physical model of any type of solid state device is presented. Application to model advanced multi-junction solar cells; Thermophotovoltaics; sensors; as well as other novel solid state devices are introduced in this presentation. The primary goal of multijunction solar cell design is to maximize the output power for a given solar spectrum [1-4]. The construction of multijunction cells places the individual junction layers in series, thereby limiting the overall output current to that of the junction layer producing the lowest current [5-7]. The solution to optimizing a multijunction design involves both the design of individual junction layers which produce an optimum output power and the design of a series-stacked configuration of these junction layers which yields the highest possible overall output current. This paper demonstrates the use of Genetic Algorithm in a two-part process to refine a given multijunction solar cell design for near-optimal output power for a desired light spectrum. This approach can similarly be utilized to optimize the parameters of any Solid state device to yield any desired performance.

Keywords: modeling, solar cells, photovoltaics, optimizations, genetic algorithm

1. Introduction

The optimization routines described in this paper use a solar cell model developed earlier at the Naval Postgraduate School (NPS) using the ATLAS device simulator by SILVACO International [8]. This model accurately predicts the electrical characteristics of a solar cell based on virtual fabrication of its physical structure.

As an example, an InGaP/GaAs/Ge triple-junction solar cell is modeled, and the device light-induced photogeneration, along with the corresponding spectral response of each layer, are illustrated in Figures 1 and 2.

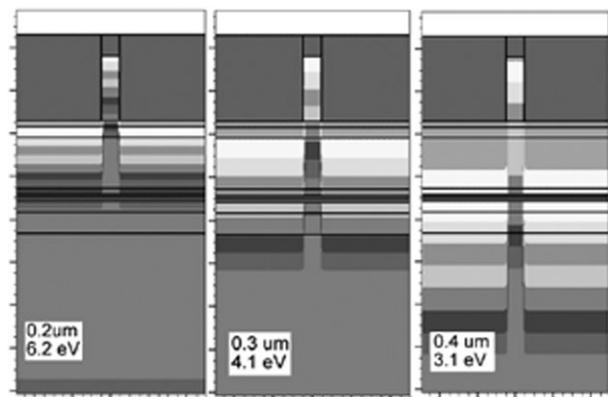


Figure 1. ATLAS photogeneration rate output for a multi-junction solar cell for different light wavelengths.

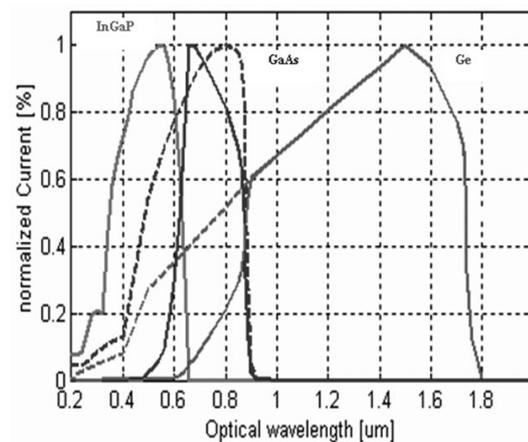


Figure 2. Spectral response of the individual cells (dashed) and of the stacked cell (continuous).

A detailed paper introducing this Novel modeling technique has been previously presented [9,10].

2. The Genetic Search Algorithm

The first step in the optimization process is to maximize the output power of each junction layer individually for several layer thicknesses. These optimum configurations will be used to ensure maximum junction layer output power as junction layer thicknesses are changed during the current-matching process. The junction layer optimization process accepts known materials for the window, emitter, base and back surface field (BSF) and determines the ideal thicknesses and doping levels for each region. The base thickness was chosen to be a dependent variable to achieve a constant overall junction layer thickness. Thus, seven independent variables remain for each overall junction layer thickness. To search all possible solutions rigorously would require an enormous amount of computational time.

Instead, a genetic algorithm [11] was used to search the solution space for the junction layer configuration producing the highest output power.

To enact a genetic algorithm for junction layer optimization, each of the eight variable junction layer parameters was encoded into a four-bit binary string. The encoded binary strings, referred to as genes, were then assembled into 32-bit binary chromosomes.

Figure 3 illustrates the construction of 32-bit binary chromosome from a gene sequence. Each chromosome fully encoded the eight variable properties of a junction layer. A set of 35 ran-

domly selected binary strings made up the initial generation of chromosomes. The encoded properties in each of these chromosomes were used to construct and simulate a junction layer ATLAS under AM0 illumination. After the simulation of an entire generation of chromosomes, child chromosomes (to make up the next generation) were formed from a mix of the genes from the best performing parent chromosomes. In addition, the best performing chromosome of each generation was passed unchanged into the next generation.

The genetic search algorithm was allowed to progress for a maximum of 20 generations. This scheme allowed a solution space of over 268 million junction layer designs to be searched to arrive near an optimal junction layer configuration for a specific thickness. Figure 4 demonstrate the improvement in the cell output power.

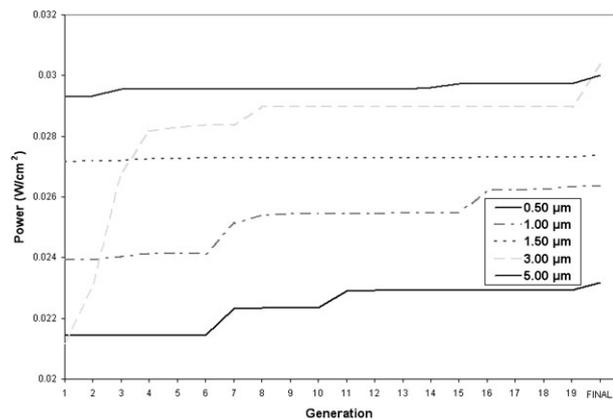


Figure 4. Improving the GaAs junction layer output power.

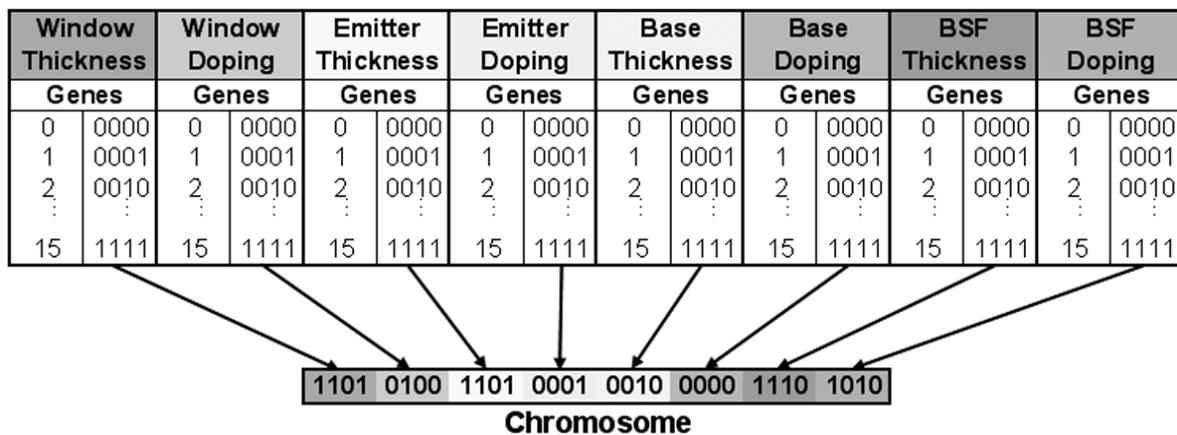


Figure 3. Construction of binary chromosomes from gene sequences.

3. Iterative Current Matching Routine

To achieve a maximum overall output current from the full multijunction cell, the current produced by each junction layer needed to be matched to the fullest extent possible. To accomplish this, we used an iterative current-matching routine beginning with an ATLAS simulation of the full multijunction cell with each junction layer thickness larger than its estimated final thickness. In this initial state, each junction layer absorbed a large amount of light and little light energy was able to penetrate to each lower junction layer successively.

To match short-circuit currents, junction layers were grouped into pairs from the top junction to the substrate junction (Figure 5). Short-circuit currents were matched within the pairs by either increasing or decreasing the thickness of the upper junction layer.

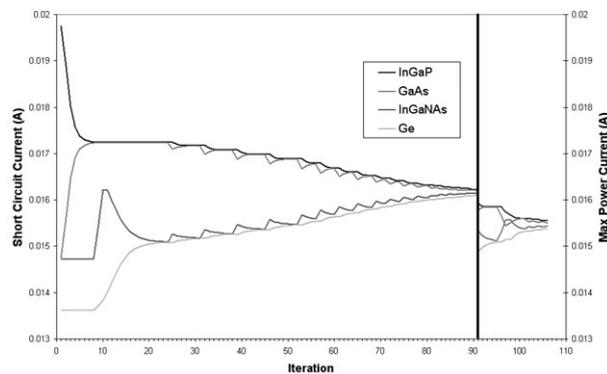


Figure 5. Progress of iterative current matching routine.

Then the junction layers were re-grouped with the other adjacent junction layer and the process repeated iteratively until the short-circuit currents of all junction layers were within 99.6% of each other. At this point, the routine changed focus to attempt to match the maximum-power currents of the individual junction layers.

The routine ceased when all junction layer maximum-power currents were matched. The optimum cell configuration could then be determined by a review of the output file for the cell with the highest overall maximum power (Figures 6 and 7).

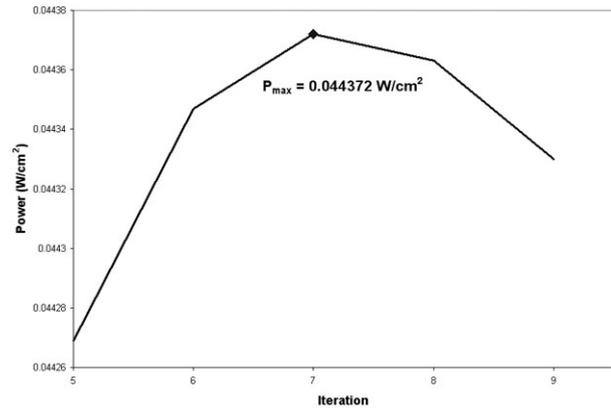


Figure 6. Optimum triple-junction cell output power.

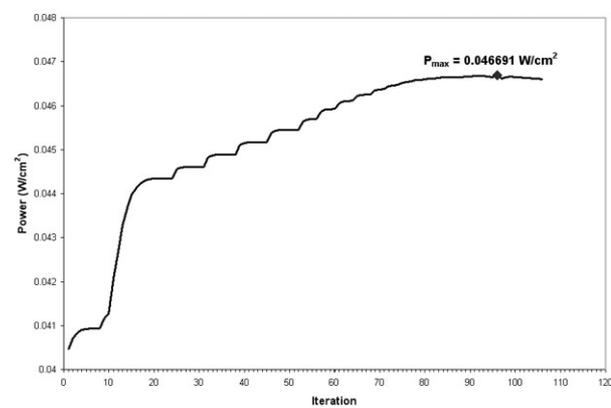


Figure 7. Optimum quad-junction cell output power.

It was necessary to ensure that each junction layer performed optimally at each of the many thicknesses used in the iterative current-matching routine. This was accomplished by using the optimized parameters from the genetic search algorithm for each junction layer. When a junction layer thickness was required that had not been specifically solved for during junction layer optimization, interpolation between known optimum values was used.

4. Results

For obvious reasons of propriety, no published reports on the detailed construction of advanced three- and four-junction solar cells could be found. This lack of an experimental baseline complicates any attempt to show the improvement over a tested design obtained by using the optimization procedure proposed in this paper.

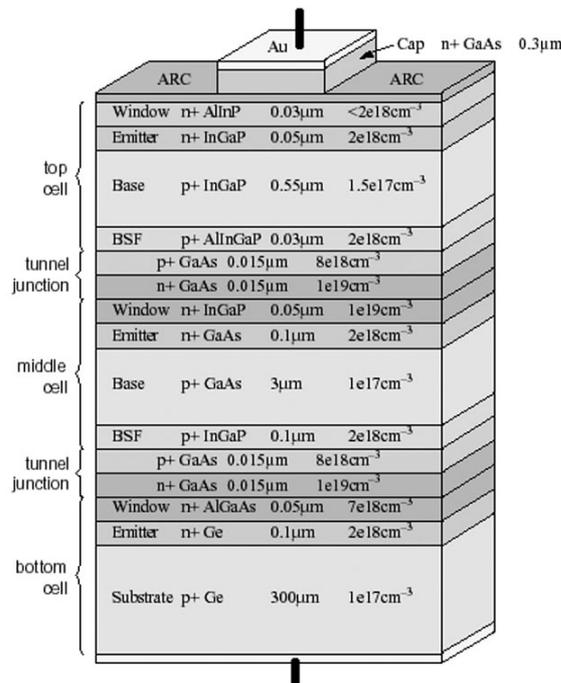


Figure 8. The new optimized triple-junction cell structure.

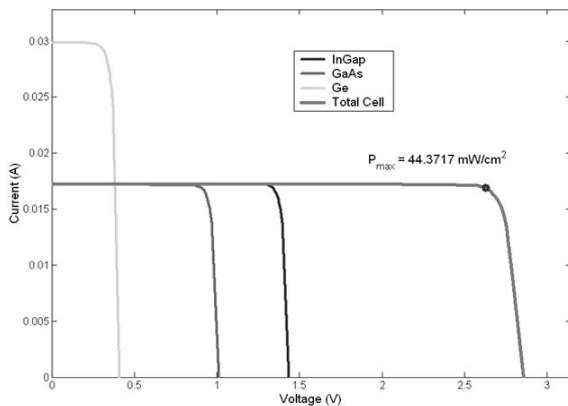


Figure 9. I-V curve for optimized 3-junction.

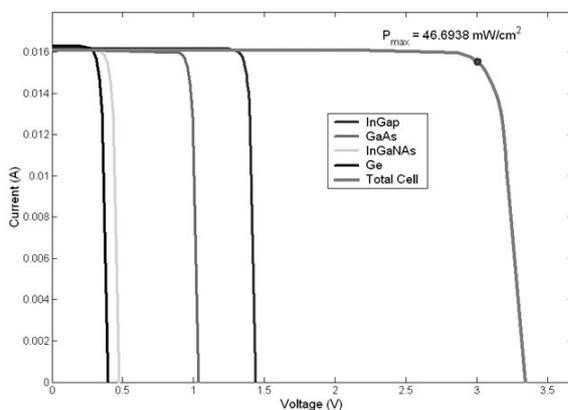


Figure 10. I-V curve for optimized 4-junction.

The advantage of this technique is therefore demonstrated using unpublished three- and four-junction solar cell designs that have been realized using ATLAS. Figures 8 and 9 illustrate the optimized triple-junction cell structure and its $I - V$ output.

Characteristics of the novel optimized quad-junction $I - V$ are presented in Figure 10, which clearly shows the unprecedented output power achieved using these optimization efforts.

The successful efforts presented here using advanced multi-junction solar cell designs, which achieved devices with record conversion efficiencies, can be similarly used to optimize many solid state devices at a very moderate cost.

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